

# Substrate Identity Revocation During Active Cognition

Capability envelope reclassifying substrate as unverified when dynamic device hash validation fails, triggering confidence reduction and non-executing cognitive mode.

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## Substrate Identity Revocation During Active Cognition

Capability envelope reclassifying substrate as unverified when dynamic device hash validation fails, triggering confidence reduction and non-executing cognitive mode without data loss. The agent continues to exist with full state but is prevented from acting until identity is re-established.

When an agent is actively executing on a substrate and the substrate's dynamic device hash validation fails, indicating that the substrate's identity continuity has been compromised, the capability envelope immediately reclassifies the substrate as unverified. The confidence governor then receives a reduced readiness signal proportional to the severity of the identity failure, and the agent transitions to non-executing cognitive mode pending substrate re-validation or migration to a verified substrate. The agent's cognitive state is preserved throughout because the cognitive domain fields are carried by the agent, not by the substrate.

## Disclosure Scope

This article describes subject matter disclosed in United States Patent Application 19/647,395. The capability envelope, dynamic device hash validation, confidence governor, and non-executing cognitive mode referenced here arise from the interaction of the platform mechanisms and the identity mechanisms of the co-pending applications incorporated by reference in that application. Nothing in this article should be read as a claim beyond the disclosure of that application.

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### **Cross-Patent Architecture** (</cross-patent-architecture>) [All 40 steps → \(/inventive-steps\)](/inventive-steps)

Cross-cutting architectural principles that compose every primitive into a coherent platform.

[Chapter 1 \(/patents/19-647395/chapters/foundation\)](/patents/19-647395/chapters/foundation).

#### **PRIMARY TECHNICAL DISCLOSURE**

–  [{step.name}, Articles \(/articles/cross-patent-architecture\)](/articles/cross-patent-architecture)

#### **SECONDARY TECHNICAL**

- [Transit Cognitive State \(/articles/cross-patent-architecture/transit-cognitive-state\)](/articles/cross-patent-architecture/transit-cognitive-state)
- [Substrate Identity Revocation During Active Cognition \(/articles/cross-patent-architecture/substrate-identity-revocation\)](/articles/cross-patent-architecture/substrate-identity-revocation)
- [Policy Freshness Across Asynchronous Execution \(/articles/cross-patent-architecture/policy-freshness-asynchronous-execution\)](/articles/cross-patent-architecture/policy-freshness-asynchronous-execution)
- [Governance Authority Evaluation via Integrity Trajectory \(/articles/cross-patent-architecture/governance-authority-integrity-trajectory\)](/articles/cross-patent-architecture/governance-authority-integrity-trajectory)
- [Discovery Agent as Schema-Conformant Index Traverser \(/articles/cross-patent-architecture/discovery-agent-schema-index-traverser\)](/articles/cross-patent-architecture/discovery-agent-schema-index-traverser)
- [Cross-Tier Navigation Primitive: World as Model \(/articles/cross-patent-architecture/cross-tier-navigation-world-as-model\)](/articles/cross-patent-architecture/cross-tier-navigation-world-as-model)

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[Cross-Patent Architecture overview → \(/cross-patent-architecture\)](#)